

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
)	
DISNEY, D.)	Examiner: Unassigned
)	
Serial No.: Not Yet Unassigned)	Art Unit:
)	
Filing Date: November 25, 2003)	
(Concurrently Herewith))	
)	
For: METHOD OF FABRICATING A)	
HIGH-VOLTAGE TRANSISTOR WITH A)	
MULTI-LAYERED EXTENDED)	
DRAIN STRUCTURE)	
)	
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Continuation Application of:)	
)	
Serial No. 10/278,551)	
Filed: October 22, 2002)	
Examiner: Nhu, David.)	
Art Unit: 2818)	
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Preliminary Amendment

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to examination of the above-identified divisional application, please
amend the application as follows: